# **74AHCV17A**

### Hex buffer Schmitt trigger Rev. 2 — 25 September 2023

**Product data sheet** 

## 1. General description

The 74AHCV17A is a hex buffer with Schmitt-trigger inputs, capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 1.8 V to 5.5 V
- Typical t<sub>pd</sub> of 3.2 ns at 5 V
- Typical  $V_{OL(p)}$  < 0.8 V at  $V_{CC}$  = 3.3 V,  $T_{amb}$  = 25 °C
- Typical  $V_{OH(v)} > 2.3 \text{ V}$  at  $V_{CC} = 3.3 \text{ V}$ ,  $T_{amb} = 25 ^{\circ}\text{C}$
- · Supports mixed-mode voltage operation on all ports
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- · ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 3000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 2000 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

# 3. Ordering information

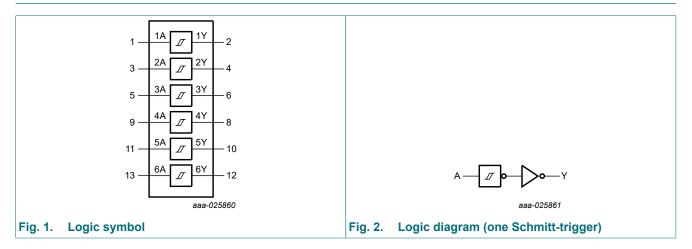
#### **Table 1. Ordering information**

Type number	Package								
	Temperature range	Name	Description	Version					
74AHCV17APW	-40 °C to +125 °C		plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1					



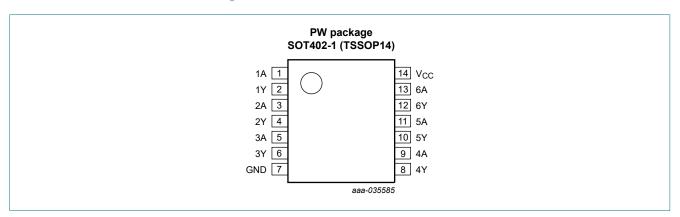
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# 4. Functional diagram



# 5. Pinning information

# 5.1. Pinning



### 5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	data input
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	data output
GND	7	ground (0 V)
Vcc	14	supply voltage

**Product data sheet** 

Hex buffer Schmitt trigger

# 6. Functional description

#### Table 3. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level.$ 

Input	Output
nA	nY
L	L
Н	Н

# 7. Limiting values

#### **Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+7.0	V
VI	input voltage	[1]	-0.5	+7.0	V
Vo	output voltage	output HIGH or LOW state [2][3]	-0.5	V <sub>CC</sub> + 0.5	V
		output power-down [2]	-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
I <sub>O</sub>	output current	V <sub>O</sub> = 0 V to V <sub>CC</sub>	-	±50	mA
I <sub>CC</sub>	supply current		-	100	mA
I <sub>GND</sub>	ground current		-100	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C} \text{ to } +125  ^{\circ}\text{C}$ [4]	-	500	mW

- [1] The minimum input voltage ratings may be exceeded if the input current ratings are observed.
- [2] The output voltage ratings may be exceeded if the output current ratings are observed.
- [3] This value is limited to 7 V maximum.
- [4] For SOT402-1 (TSSOP14) package: P<sub>tot</sub> derates linearly with 7.3 mW/K above 81 °C.

# 8. Recommended operating conditions

#### Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CC}$	supply voltage		1.8	5.0	5.5	V
V <sub>I</sub>	input voltage		0	-	5.5	V
V <sub>O</sub>	output voltage	output HIGH or LOW state	0	-	V <sub>CC</sub>	V
		output power-down	0	-	5.5	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	50	ms/V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	20	ms/V
		V <sub>CC</sub> = 4.5 V to 5.5 V	-	-	1	ms/V

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# 9. Static characteristics

#### **Table 6. Static characteristics**

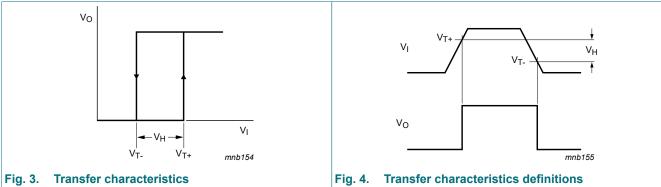
Voltages are referenced to GND (ground = 0 V).

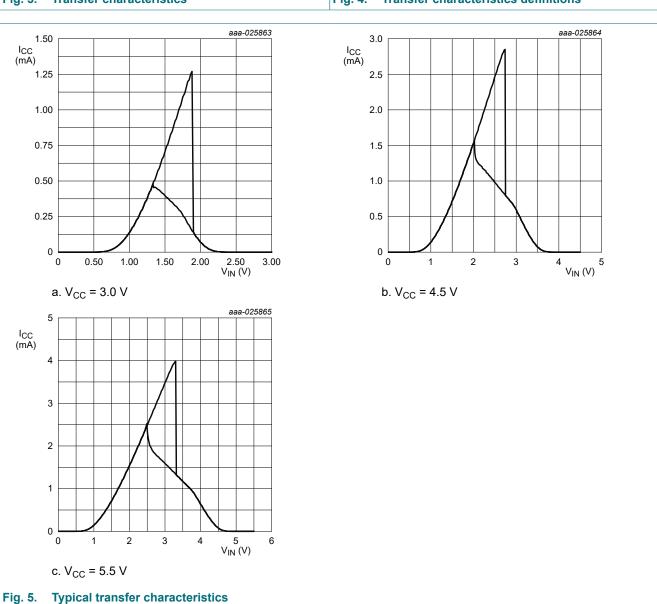
Symbol Parameter		Conditions		25 °C	:	-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V <sub>T+</sub>	positive-going	V <sub>CC</sub> = 1.8 V	-	-	1.65	-	1.65	-	1.65	V
	threshold voltage	V <sub>CC</sub> = 2.3 V	-	-	1.85	-	1.85	-	1.85	V
	Voltage	V <sub>CC</sub> = 3.0 V	-	-	2.2	-	2.2	-	2.2	V
		V <sub>CC</sub> = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
		V <sub>CC</sub> = 5.5 V	-	-	3.85	-	3.85	-	3.85	V
V <sub>T-</sub> negative-going		V <sub>CC</sub> = 1.8 V	0.15	-	-	0.15	-	0.15	-	V
	threshold voltage	V <sub>CC</sub> = 2.3 V	0.45	-	-	0.45	-	0.45	-	V
	voitage	V <sub>CC</sub> = 3.0 V	0.9	-	-	0.9	-	0.9	-	V
		V <sub>CC</sub> = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
		V <sub>CC</sub> = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
V <sub>H</sub>	hysteresis	V <sub>CC</sub> = 1.8 V	0.15	-	1.05	0.15	1.05	0.15	1.05	V
	voltage	V <sub>CC</sub> = 2.3 V	0.2	-	1.1	0.2	1.1	0.2	1.1	V
		V <sub>CC</sub> = 3.0 V	0.3	-	1.2	0.3	1.2	0.3	1.2	V
		V <sub>CC</sub> = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	V
		V <sub>CC</sub> = 5.5 V	0.5	-	1.6	0.5	1.6	0.5	1.6	V
V <sub>OH</sub>	HIGH-level	$V_I = V_{T+}$ or $V_{T-}$								
	output voltage	I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 1.8 V	1.7	1.8	-	1.7	-	1.7	-	V
		I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I <sub>O</sub> = -50 μA; V <sub>CC</sub> = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		$I_O = -8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.48	-	V
		I <sub>O</sub> = -16 mA; V <sub>CC</sub> = 4.5 V	3.94	-	-	3.80	-	3.80	-	V
V <sub>OL</sub>	LOW-level	$V_I = V_{T+}$ or $V_{T-}$								
	output voltage	I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 1.8 V	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 3.0 V$	-	0	0.1	-	0.1	-	0.1	V
		I <sub>O</sub> = 50 μA; V <sub>CC</sub> = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.44	V
		I <sub>O</sub> = 16 mA; V <sub>CC</sub> = 4.5 V	-	-	0.44	-	0.55	-	0.55	V
I <sub>OFF</sub>	power-off leakage current	$V_1$ or $V_0$ = GND to 5.5 V; $V_{CC}$ = 0 V	-	-	0.5	-	5	-	5	μA
lı	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0$ V to 5.5 V	-	-	±0.1	-	±1	-	±1	μΑ
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	2	-	20	-	20	μΑ

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### 9.1. Transfer characteristics waveforms





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# 10. Dynamic characteristics

**Table 7. Dynamic characteristics** 

GND = 0 V. For test circuit see Fig. 7.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C t	Unit	
				Min	Typ[1]	Max	Min	Max	Min	Max	
t <sub>pd</sub>	propagation	nA to nY; see Fig. 6	[2]								
	delay	V <sub>CC</sub> = 2.3 V to 2.7 V									
		C <sub>L</sub> = 15 pF		-	5.3	19.7	1	22	1	23.6	ns
		C <sub>L</sub> = 50 pF		-	7.3	24	1	27	1	29.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V									
		C <sub>L</sub> = 15 pF		-	4.1	12.8	1	15	1	16.2	ns
		C <sub>L</sub> = 50 pF		-	5.7	16.3	1	18.5	1	20.0	ns
		V <sub>CC</sub> = 4.5 V to 5.5 V									
		C <sub>L</sub> = 15 pF		-	3.2	8.6	1	10	1	10.7	ns
		C <sub>L</sub> = 50 pF		-	4.5	10.6	1	12	1	12.9	ns
Cı	input capacitance	$V_I = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$		-	2	6	-	6	-	6	pF
Co	output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$		-	5	-	-	-	-	-	pF
C <sub>PD</sub>	power dissipation capacitance	per buffer; $V_{CC}$ = 5 V; $C_L$ = 0 pF; f = 10 MHz; $V_I$ = GND to $V_{CC}$	[3]	-	15	-	-	-	-	-	pF

<sup>[1]</sup> Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 2.5 V, 3.3 V, and 5 V respectively, unless otherwise specified.

 $f_i$  = input frequency in MHz;  $f_o$  = output frequency in MHz;

 $C_L$  = output load capacitance in pF;  $V_{CC}$  = supply voltage in V.

#### **Table 8. Noise characteristics**

GND = 0 V. For test circuit see Fig. 7.

Symbol	Parameter	Conditions	Т	T <sub>amb</sub> = 25 °C			
			Min	Тур	Max		
$V_{CC} = 3.3$	3 V; C <sub>L</sub> = 50 pF		,				
V <sub>OL(p)</sub>	LOW-level output voltage (peak)		-	0.3	0.8	V	
V <sub>OL(v)</sub>	LOW-level output voltage (valley)		-0.8	-0.1	-	V	
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	3.0	-	V	
V <sub>IH(AC)</sub>	AC HIGH-level input voltage (dynamic)		2.31	-	-	V	
V <sub>IL(AC)</sub>	AC LOW-level input voltage (dynamic)		-	-	0.99	V	
V <sub>CC</sub> = 5.0	) V; C <sub>L</sub> = 50 pF						
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.6	-	V	
V <sub>OL(v)</sub>	LOW-level output voltage (valley)		-	-0.4	-	V	
V <sub>OH(v)</sub>	HIGH-level output voltage (valley)		-	4.5	-	V	
V <sub>IH(AC)</sub>	AC HIGH-level input voltage (dynamic)		3.5	-	-	V	
V <sub>IL(AC)</sub>	AC LOW-level input voltage (dynamic)		-	-	1.5	V	

Typical values are included at V<sub>amb</sub> 25 of an a V<sub>CC</sub> 2.5 v, o.s. t<sub>pd</sub> is the same as t<sub>pLH</sub> and t<sub>pHL</sub>.
 C<sub>PD</sub> is used to determine the dynamic power dissipation P<sub>D</sub> (μW).
 P<sub>D</sub> = C<sub>PD</sub> × V<sub>CC</sub> <sup>2</sup> × f<sub>i</sub> + ∑ (C<sub>L</sub> × V<sub>CC</sub> <sup>2</sup> × f<sub>o</sub>) where:

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#### 10.1. Waveforms and test circuit

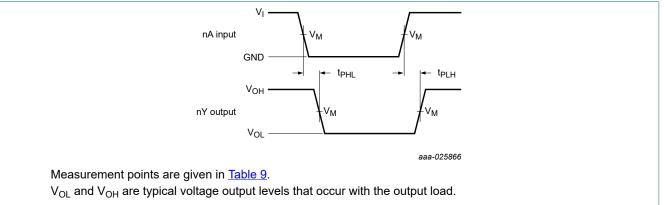
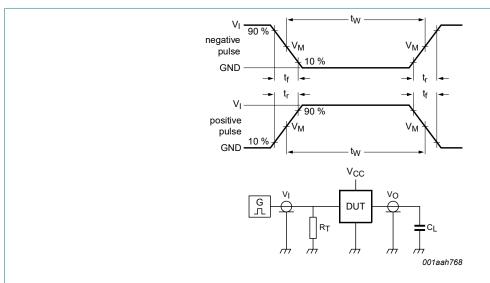


Fig. 6. Propagation delay input (nA) to output (nY)

**Table 9. Measurement points** 

Input	Output
$V_{M}$	$V_{M}$
0.5 × V <sub>CC</sub>	0.5 × V <sub>CC</sub>



Test data is given in Table 10.

Definitions test circuit:

 $R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator;

C<sub>L</sub> = Load capacitance including jig and probe capacitance;

S1 = Test selection switch.

# Fig. 7. Test circuit for measuring switching times

#### Table 10. Test data

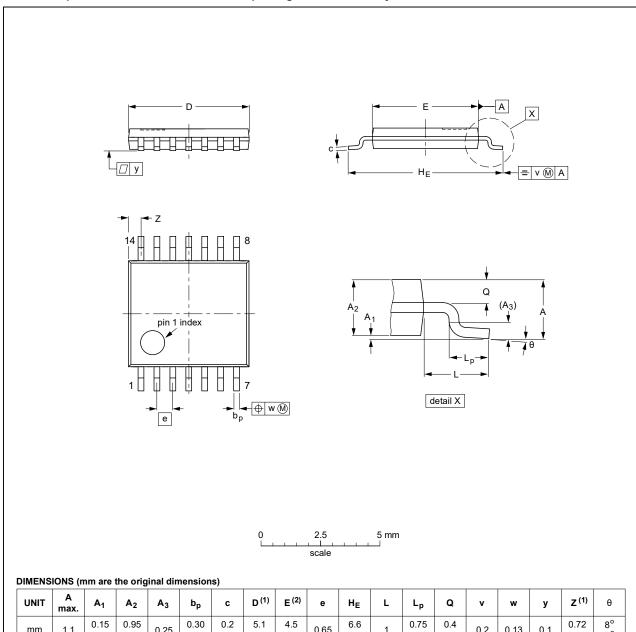
Input		Load	Test
V <sub>I</sub>	t <sub>r</sub> , t <sub>f</sub>	CL	
GND to V <sub>CC</sub>	3.0 ns	15 pF, 50 pF	t <sub>PLH</sub> , t <sub>PHL</sub>

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# 11. Package outline

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1



UNIT	. A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(2)</sup>	е	HE	L	Lp	Q	v	w	у	Z <sup>(1)</sup>	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	5.1 4.9	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.72 0.38	8° 0°

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

	OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSIO	VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
	SOT402-1		MO-153				<del>99-12-27</del> 03-02-18

Fig. 8. Package outline SOT402-1 (TSSOP14)

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# 12. Abbreviations

#### **Table 11. Abbreviations**

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model

# 13. Revision history

### **Table 12. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHCV17A v.2	20230925	Product data sheet	-	74AHCV17A v.1	
Modifications	<ul> <li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> <li>Section 2: ESD specification updated according to the latest JEDEC standard.</li> <li>Table 4: Derating values for P<sub>tot</sub> total power dissipation updated.</li> </ul>				
74AHCV17A v.1	20161206	Product data sheet	-	-	

#### Hex buffer Schmitt trigger

## 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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